



Preliminary

TFT LCD Preliminary Specification

MODEL NO.: M215H3-L01

Customer : _____
Approved by : _____
Note :

核准時間	部門	審核	角色	投票
2009-07-14 10:19:00	MTR 產品管理處	<div style="text-align: center; margin-top: -20px;"> 吳 2009.07.14 柏勳 </div>	Director	Accept

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REVISION HISTORY

Version	Date	Section	Description																																																												
Ver 0.0	May,12,'09	All	M215H3-L01 Specifications was first issued。																																																												
Ver 1.0	June,25,'09	All	M215H3-L01 Preliminary specification was first issued.																																																												
		1.5	Update module weight to (typ.) 1850g & (max) 1900g																																																												
		2.2.2	Update light bar Peak pulse current 30mA																																																												
		3.1	Update light bar Input Voltage 29.7(typ), 32.4 (max)																																																												
		3.3	Confirm TFT Module power consumption (5.3W) → 5.25W																																																												
		5.1	Update LED light bar connector type: 91500-01001-H01 (Aces)																																																												
		6.1	Insert Note (2) Mating Wire Cable Connector Part No.: FI-X30H(JAE) or FI-X30HL(JAE) Note (3) Mating FFC Cable Connector Part No.: 91500-01001-H01(ACES)																																																												
			INPUT SIGNAL TIMING SPECIFICATIONS																																																												
			Add LVDS clock item:																																																												
			<table border="1"> <thead> <tr> <th>Item</th><th>Symbol</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th></tr> </thead> <tbody> <tr> <td>Input cycle to cycle jitter</td><td>T_{rcl}</td><td>—</td><td>—</td><td>200</td><td>ps</td></tr> <tr> <td>Spread spectrum modulation range</td><td>F_{clkin_mod}</td><td>$F_{clkin} - 2\%$</td><td>—</td><td>$F_{clkin} + 2\%$</td><td>MHz</td></tr> <tr> <td>Spread spectrum modulation frequency</td><td>F_{SSM}</td><td></td><td></td><td>200</td><td>KHz</td></tr> </tbody> </table>						Item	Symbol	Min.	Typ.	Max.	Unit	Input cycle to cycle jitter	T_{rcl}	—	—	200	ps	Spread spectrum modulation range	F_{clkin_mod}	$F_{clkin} - 2\%$	—	$F_{clkin} + 2\%$	MHz	Spread spectrum modulation frequency	F_{SSM}			200	KHz																															
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			Add Note(1), Note(2), Note(3)																																																												
		7.1	Confirm Color Chromaticity: Min.Value = Typ. – 0.03, and Max.Value = Typ. + 0.03																																																												
			<table border="1"> <thead> <tr> <th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th><th>Note</th></tr> </thead> <tbody> <tr> <td>(0.636)</td><td></td><td></td><td></td><td></td></tr> <tr> <td>(0.348)</td><td></td><td></td><td></td><td></td></tr> <tr> <td>(0.327)</td><td></td><td></td><td></td><td></td></tr> <tr> <td>Typ – 0.03</td><td>(0.611)</td><td>Typ + 0.03</td><td></td><td>(1), (5)</td></tr> <tr> <td></td><td>(0.154)</td><td></td><td></td><td></td></tr> <tr> <td></td><td>(0.059)</td><td></td><td></td><td></td></tr> <tr> <td></td><td>0.313</td><td></td><td></td><td></td></tr> <tr> <td></td><td>0.329</td><td></td><td></td><td></td></tr> <tr> <td></td><td>200</td><td>250</td><td>---</td><td>cd/m²</td></tr> <tr> <td></td><td></td><td></td><td></td><td>(4), (5)</td></tr> </tbody> </table>						Min.	Typ.	Max.	Unit	Note	(0.636)					(0.348)					(0.327)					Typ – 0.03	(0.611)	Typ + 0.03		(1), (5)		(0.154)					(0.059)					0.313					0.329					200	250	---	cd/m ²					(4), (5)
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		8.1	Confirm (2) Box dimensions: 565(L) X 295 (W) X 375 (H) mm --Original 563(L) X 390 (W) X 375 (H) mm – New																																																												
		10.0	(3) Weight: 26.29 Kg (12 modules per box) --Original 24.96 Kg (12 modules per box) – New																																																												
		11.4	Add CH 10.0 Reliability Test																																																												
		11.5	Add CH 11.4. Storage																																																												
			Add CH 11.5. Operation condition guide																																																												



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1. GENERAL DESCRIPTION

1.1 OVERVIEW

The M215H3-L01 model is a 21.5 inch wide TFT-LCD module with LED Backlight Unit and a 30-pin 2ch-LVDS interface. This module supports 1920 x 1080 Full HD (16:9 wide screen) mode and displays up to 16.7 millions colors. The converter module for the Backlight Unit is not built in.

1.2 FEATURES

- Super wide viewing angle
- High contrast ratio
- Fast response time
- Low power consumption
- FULL HD(1920 x 1080 pixels) resolution
- DE (Data Enable) only mode
- LVDS (Low Voltage Differential Signaling) interface
- RoHS compliance.

1.3 APPLICATION

- Workstation & desktop monitor
- Display terminals for AV application

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Diagonal size	546.86 (21.53")	mm	
Active Area	476.64 x 268.11	mm	(1)
Bezel Opening Area	479.8 (H) x 271.3 (V)	mm	
Driver Element	a-Si TFT active matrix	-	-
Pixel Number	1920 x R.G.B. x 1080	pixel	-
Pixel Pitch	0.248(H) x 0.248(V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7 millions	color	-
Transmissive Mode	Normally White	-	-
Surface Treatment	Hard coating (3H), AG (Haze 25%)	-	-
Module Power Consumption	12.55	Watt	(2)

1.5 MECHANICAL SPECIFICATIONS

Item	Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	495.1	495.6	mm	(1)
	Vertical(V)	291.7	292.2	mm	
	Depth(D)	9.5	10	mm	
Weight	--	1850	1900	g	
I/F connector mounting position	The mounting inclination of the connector makes the screen center within ±0.5 mm as the horizontal.				

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2) Please refer to sec. 3.1 & 3.2 in this document for more information of power consumption



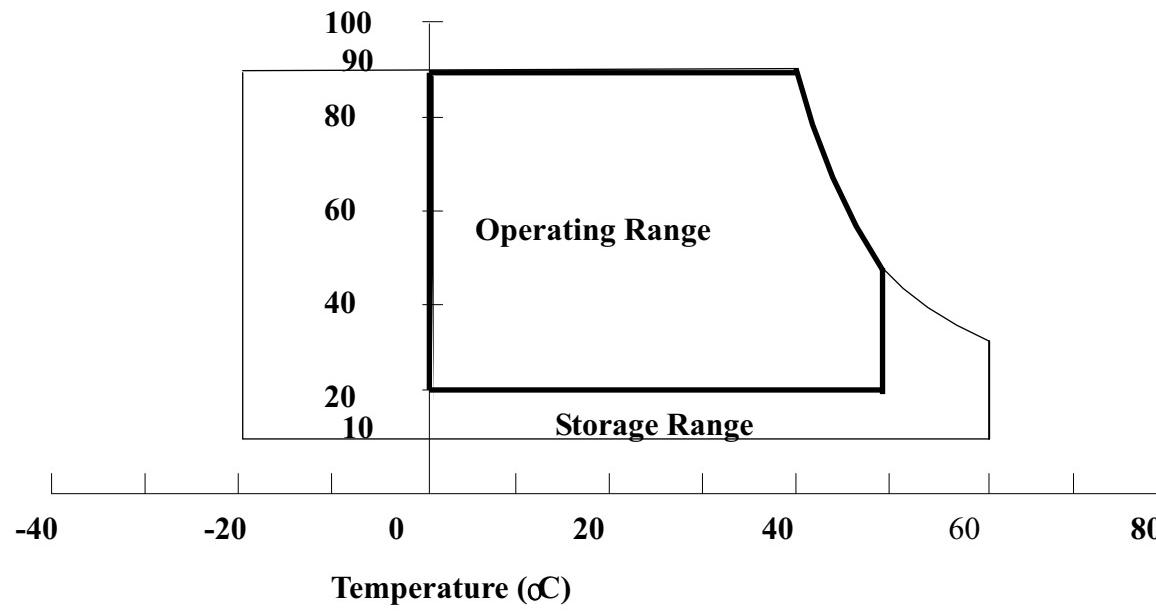
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2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T_{OP}	0	+50	°C	(1), (2)
Shock (Non-Operating)	S_{NOP}	-	50	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	-	1.5	G	(4), (5)
LCD Cell Life Time	L_{CELL}	50,000	-	Hrs	MTBF based

Relative Humidity (%RH)



Note (1) Temperature and relative humidity range is shown in the figure below.

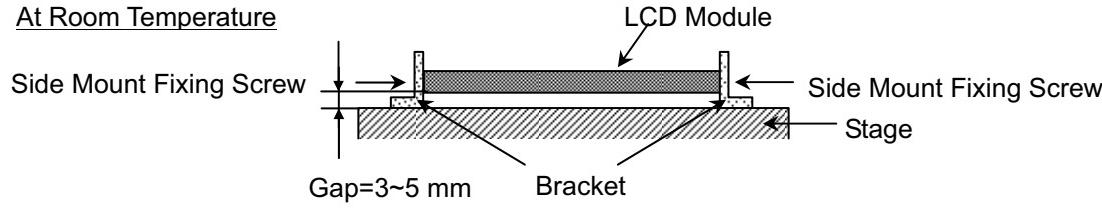
- (a) 90% RH Max. ($T_a \leq 40^{\circ}\text{C}$).
- (b) Wet-bulb temperature should be 39°C Max. ($T_a > 40^{\circ}\text{C}$).
- (c) No condensation.

Note (2) The temperature of panel surface should be 0°C Min. and 60°C Max.

Note (3) 50G, 11 ms, half-sine wave, 1 time for $\pm X, \pm Y, \pm Z$.

Note (4) 10 ~ 300 Hz, sweep rate 10 min / cycle , 30 min for X,Y,Z axis

Note (5) Upon the Vibration and Shock tests, the fixture used to hold the module must be firm and rigid enough to prevent the module from twisting or bending by the fixture.





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2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	Vcc	-0.3	+6	V	(1)

2.2.2 BACK LIGHT UNIT

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Light Bar Input Current	I _f	---	20	30	mA	(1)
Light Bar Peak pulse current	I _p	---	---	30		
Light Bar Input Voltage	V _r	---	29.7	32.4		

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

3. ELECTRICAL CHARACTERISTICS

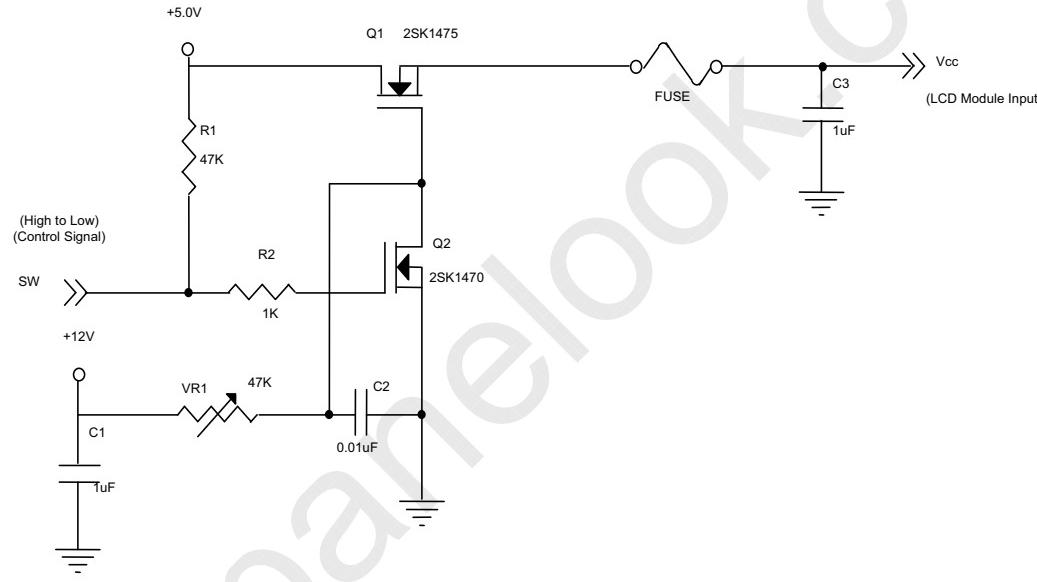
3.1 TFT LCD MODULE

T_a = 25 ± 2 °C

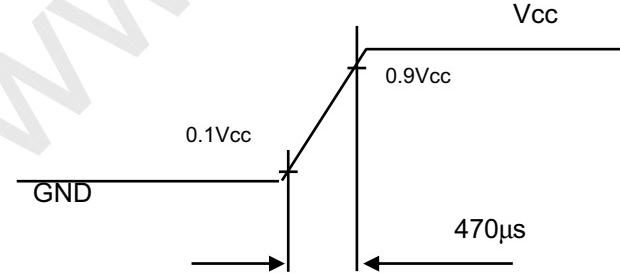
Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V _{CC}	4.5	5.0	5.5	V	-
Ripple Voltage	V _{RP}	-	--	300	mV	V _{p-p}
Power on Rush Current	I _{RUSH}	-	--	3	A	(2)
Power Supply Current	I _{CC}	White	-	0.51	A	(3)a
		Black	-	1.05	A	(3)b
		Vertical Stripe	-	1.06	A	(3)c
Power consumption(without Backlight Unit)	P _{LCD}	-	5.3	6.3	W	(4)
LVDS differential input voltage	V _{ID}	100	-	600	mV	
LVDS common input voltage	V _{IC}	1	1.2	1.4	V	

Note (1) The module should be always operated within above ranges.

Note (2) Power on rush current measurement conditions:



V_{CC} rising time is 470μs





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Note (3) The specified power supply current is under the conditions at $V_{cc} = 5.0$ V, $T_a = 25 \pm 2$ °C, $f_v = 60$ Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern



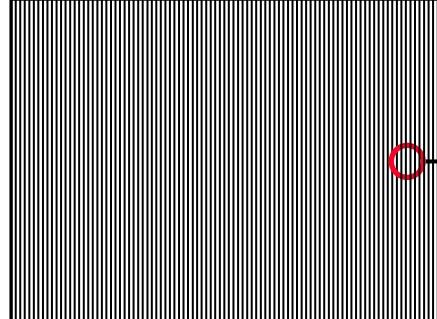
b. Black Pattern



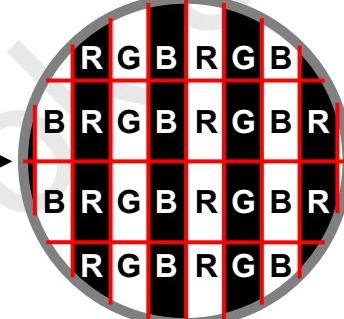
Active Area

Active Area

c. Vertical Stripe Pattern

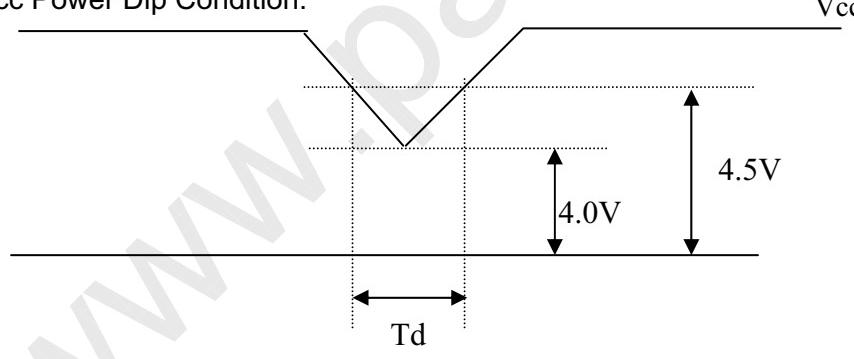


Active Area



Note (4)The power consumption is specified at the pattern with the maximum current

3.2 V_{cc} Power Dip Condition:



Dip condition: $4.0V \leq V_{cc} \leq 4.5V, T_d \leq 20ms$

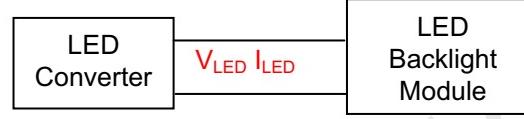


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3.3 BACKLIGHT UNIT

 $T_a = 25 \pm 2 ^\circ C$

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Light Bar Input Voltage	V_{LED}	25.2	29.7	32.4	V_{DC}	(Duty 100%)
Light Bar Input Current	I_{LED}	19.4	20	20.6	mA_{DC}	(Duty 100%) per string
Power Consumption	P_{LED}	--	7.3	--	W	(1)
LED Life Time	L_{BL}	25000	--	--	Hrs	(2)

Note (1) $P_{LED} = (I_{LED} \times 12) \times V_{LED}$, LED matrix is 9S12P.Note (2) The lifetime of LED is defined as the time when it continues to operate under the conditions at $T_a = 25 \pm 2 ^\circ C$ and $I = 20 \text{ mA(Per EA)}$ until the brightness becomes $\leq 50\%$ of its original value.

3.4 LIGHTBAR Connector Pin Assignment

Connector: 91500-01001-H01 宏致(Aces)

LED:PT01B1XX

Input Connector pin assignment:

(1) Input connector pin assignment: CN1

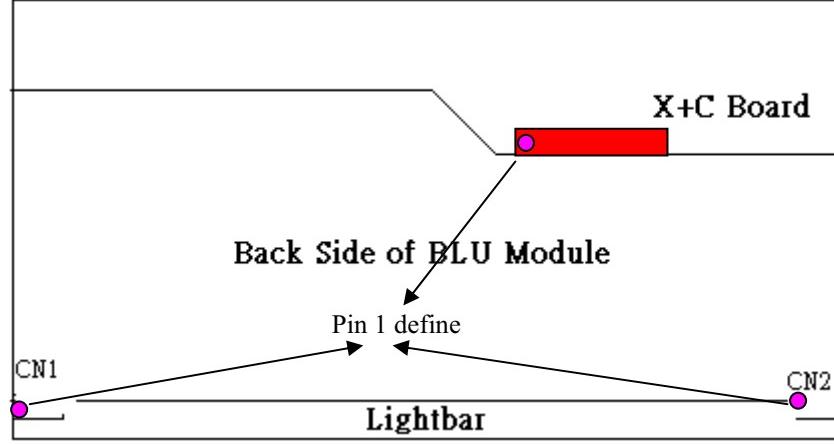
Input connector CN1		Comments
(vendor) (Aces)	(type) 91500-01001-H01	
Pin	Function	
1	VLED (29.7V)	Input voltage Power Supply + (29.7V.typ)
2	VLED (29.7V)	Input voltage Power Supply + (29.7V.typ)
3	NC	No connect
4	NC	No connect
5	LED1	LED1 negative polarity
6	LED2	LED2 negative polarity
7	LED3	LED3 negative polarity
8	LED4	LED4 negative polarity
9	LED5	LED5 negative polarity
10	LED6	LED6 negative polarity



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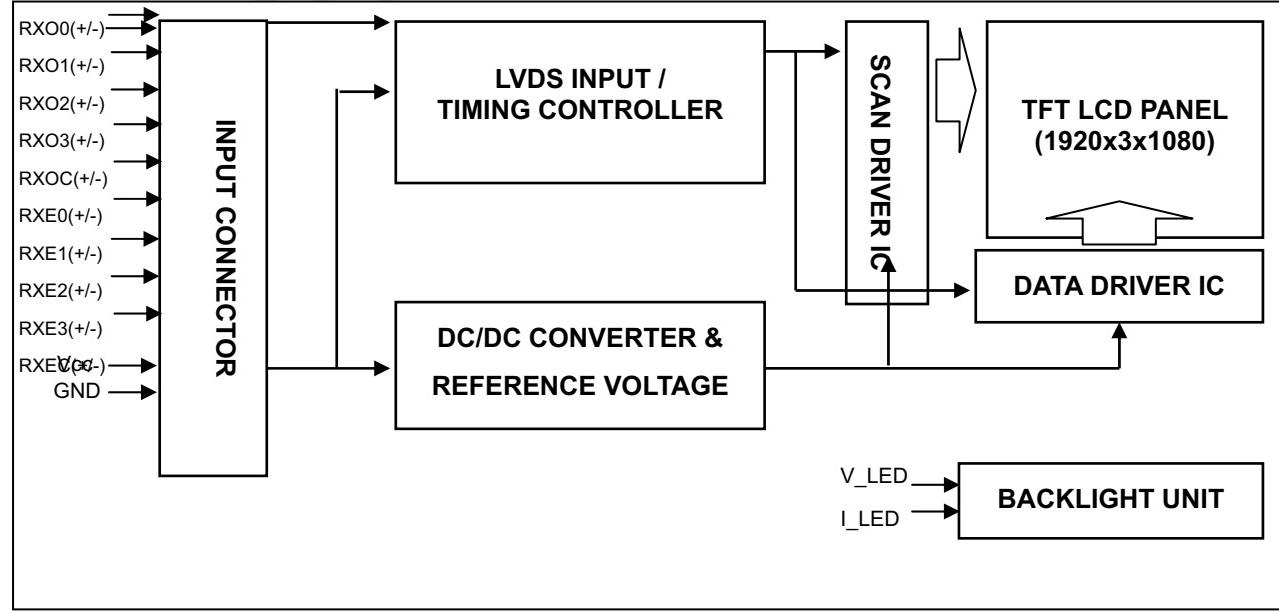
(2) Input connector pin assignment: CN2

Input connector CN2		Comments
(vendor) (Aces)	(type) 91500-01001-H01	
Pin	Function	
1	LED7	LED7 negative polarity
2	LED8	LED8 negative polarity
3	LED9	LED9 negative polarity
4	LED10	LED10 negative polarity
5	LED11	LED11 negative polarity
6	LED12	LED12 negative polarity
7	NC	No connect
8	NC	No connect
9	VLED (29.7V)	Input voltage Power Supply + (29.7V.typ)
10	VLED (29.7V)	Input voltage Power Supply + (29.7V.typ)



4. BLOCK DIAGRAM

4.1 TFT LCD MODULE





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5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Name	Description
1	RXO0-	Negative LVDS differential data input. Channel O0 (odd)
2	RXO0+	Positive LVDS differential data input. Channel O0 (odd)
3	RXO1-	Negative LVDS differential data input. Channel O1 (odd)
4	RXO1+	Positive LVDS differential data input. Channel O1 (odd)
5	RXO2-	Negative LVDS differential data input. Channel O2 (odd)
6	RXO2+	Positive LVDS differential data input. Channel O2 (odd)
7	GND	Ground
8	RXOC-	Negative LVDS differential clock input. (odd)
9	RXOC+	Positive LVDS differential clock input. (odd)
10	RXO3-	Negative LVDS differential data input. Channel O3(odd)
11	RXO3+	Positive LVDS differential data input. Channel O3 (odd)
12	RXE0-	Negative LVDS differential data input. Channel E0 (even)
13	RXE0+	Positive LVDS differential data input. Channel E0 (even)
14	GND	Ground
15	RXE1-	Negative LVDS differential data input. Channel E1 (even)
16	RXE1+	Positive LVDS differential data input. Channel E1 (even)
17	GND	Ground
18	RXE2-	Negative LVDS differential data input. Channel E2 (even)
19	RXE2+	Positive LVDS differential data input. Channel E2 (even)
20	RXEC-	Negative LVDS differential clock input. (even)
21	RXEC+	Positive LVDS differential clock input. (even)
22	RXE3-	Negative LVDS differential data input. Channel E3 (even)
23	RXE3+	Positive LVDS differential data input. Channel E3 (even)
24	GND	Ground
25	NC	Not connection, this pin should be open.
26	NC	Not connection, this pin should be open.
27	NC	Not connection, this pin should be open.
28	VCC	+5.0V power supply
29	VCC	+5.0V power supply
30	VCC	+5.0V power supply

Note (1) Connector Part No.: 093G30-B2001A(STARCONN) or 187045-30091(P-TWO)

Note (2) Mating Wire Cable Connector Part No.: FI-X30H(JAE) or FI-X30HL(JAE)

Note (3) Mating FFC Cable Connector Part No.: 91500-01001-H01(ACES)

Note (4) The first pixel is odd.

Note (5) Input signal of even and odd clock should be the same timing.



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5.2 LVDS DATA MAPPING TABLE

LVDS Channel O0	LVDS output	D7	D6	D4	D3	D2	D1	D0
LVDS Channel O1	Data order	OG0	OR5	OR4	OR3	OR2	OR1	OR0
LVDS Channel O2	LVDS output	D18	D15	D14	D13	D12	D9	D8
LVDS Channel O2	Data order	OB1	OB0	OG5	OG4	OG3	OG2	OG1
LVDS Channel O3	LVDS output	D26	D25	D24	D22	D21	D20	D19
LVDS Channel O3	Data order	DE	NA	NA	OB5	OB4	OB3	OB2
LVDS Channel E0	LVDS output	D23	D17	D16	D11	D10	D5	D27
LVDS Channel E0	Data order	NA	OB7	OB6	OG7	OG6	OR7	OR6
LVDS Channel E1	LVDS output	D7	D6	D4	D3	D2	D1	D0
LVDS Channel E1	Data order	EG0	ER5	ER4	ER3	ER2	ER1	ER0
LVDS Channel E1	LVDS output	D18	D15	D14	D13	D12	D9	D8
LVDS Channel E1	Data order	EB1	EB0	EG5	EG4	EG3	EG2	EG1
LVDS Channel E2	LVDS output	D26	D25	D24	D22	D21	D20	D19
LVDS Channel E2	Data order	DE	NA	NA	EB5	EB4	EB3	EB2
LVDS Channel E3	LVDS output	D23	D17	D16	D11	D10	D5	D27
LVDS Channel E3	Data order	NA	EB7	EB6	EG7	EG6	ER7	ER6



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5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Red(253)	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale Of Green	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Green(253)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1	0	0	0	0	0	
	Green(254)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
Gray Scale Of Blue	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

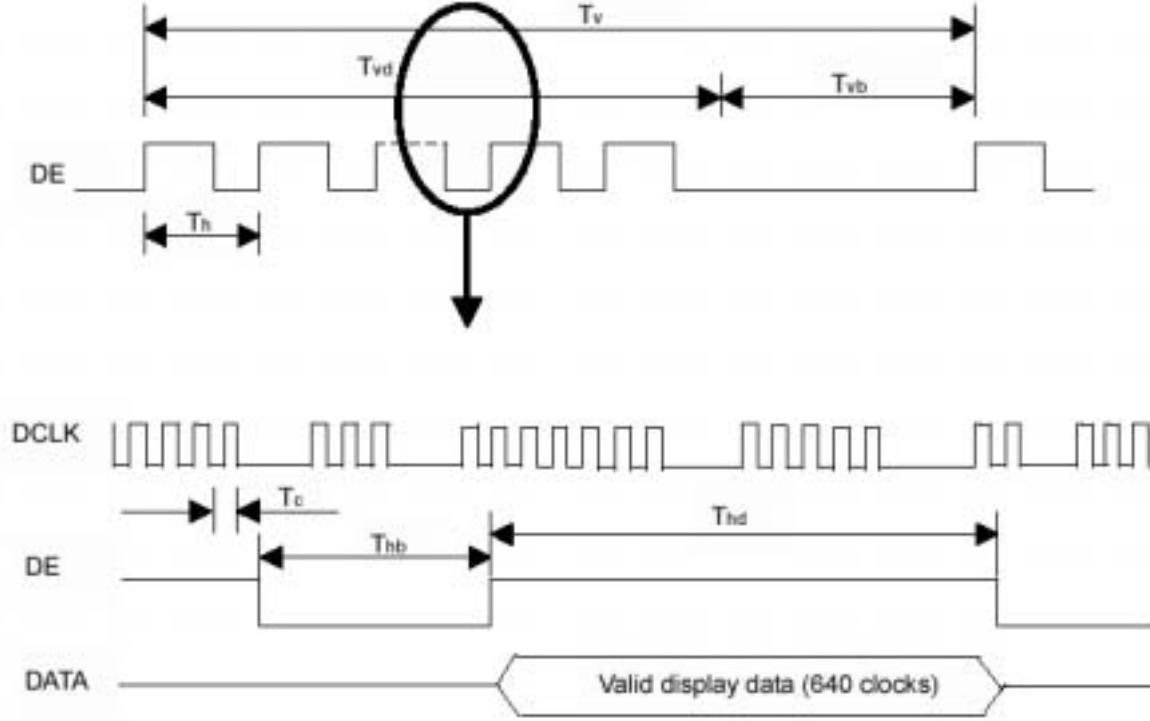
6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

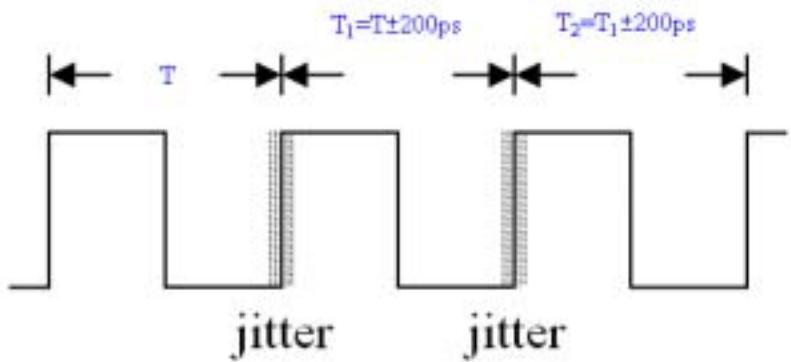
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	F _c	58.54	74.25	97.98	MHz	-
	Period	T _c	-	13.47	-	ns	
	Input cycle to cycle jitter	T _{rcl}	-	-	200	ps	(1)
	Spread spectrum modulation range	F _{clkin_mod}	F _{clkin} -2%	-	F _{clkin} +2%	MHz	(2)
	Spread spectrum modulation frequency	F _{SSM}			200	KHz	
	High Time	T _{ch}	-	4/7	-	T _c	-
LVDS Data	Low Time	T _{cl}	-	3/7	-	T _c	-
	Setup Time	T _{lvs}	600	-	-	ps	-
Vertical Active Display Term	Hold Time	T _{lvh}	600	-	-	ps	-
	Frame Rate	F _r	50	60	75	Hz	T _v =T _{vd} +T _{vb}
	Total	T _v	1115	1125	1136	Th	-
Horizontal Active Display Term	Display	T _{vd}	1080	1080	1080	Th	-
	Blank	T _{vb}	T _v -T _{vd}	45	T _v -T _{vd}	Th	-
	Total	T _h	1050	1100	1150	T _c	T _{hd} =T _{hb}
Horizontal Active Display Term	Display	T _{hd}	960	960	960	T _c	-
	Blank	T _{hb}	T _h -T _{hd}	140	T _h -T _{hd}	T _c	-

Note : Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

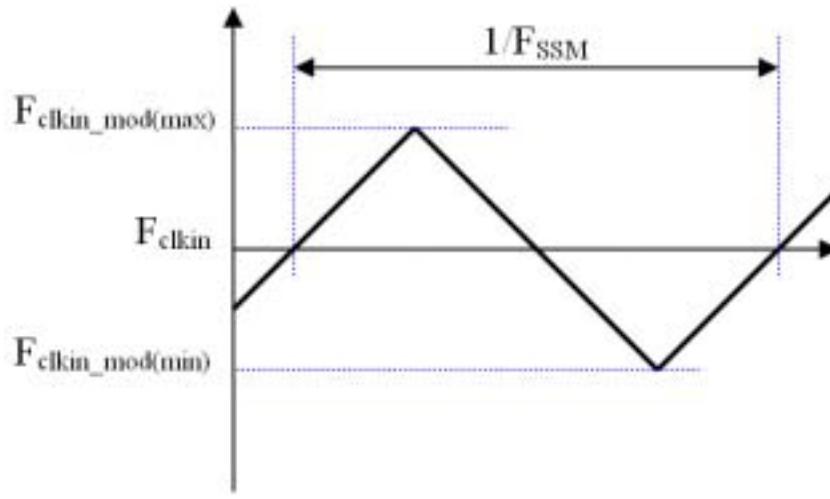
INPUT SIGNAL TIMING DIAGRAM



Note (1) The input clock cycle-to-cycle jitter is defined as below figures. $T_{ccl} = |T_1 - T_2|$

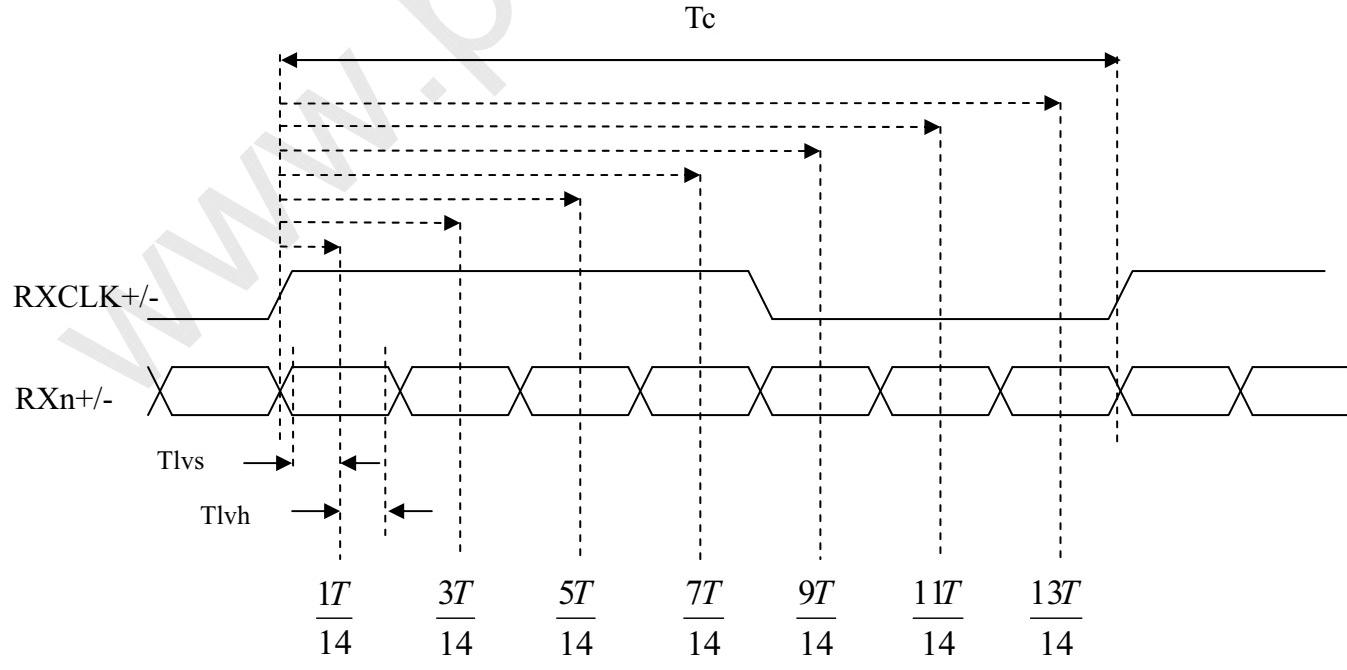


Note (2) The SSCG (Spread spectrum clock generator) is defined as below figures.



Note (3) The LVDS timing diagram and setup/hold time is defined and showing as the following figures.

LVDS RECEIVER INTERFACE TIMING DIAGRAM

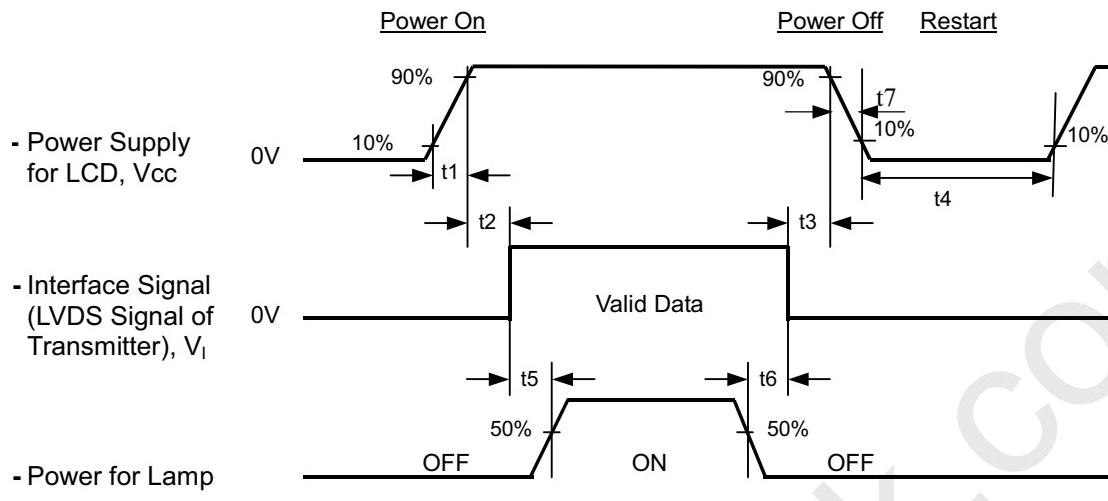




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6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Timing Specifications:

$$0.5 < t_1 \leq 10 \text{ msec}$$

$$0 < t_2 \leq 50 \text{ msec}$$

$$0 < t_3 \leq 50 \text{ msec}$$

$$t_4 \geq 500 \text{ msec}$$

$$t_5 \geq 450 \text{ msec}$$

$$t_6 \geq 90 \text{ msec}$$

$$5 \leq t_7 \leq 100 \text{ msec}$$

Note.

- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) Apply the light bar voltage within the LCD operation range. When the backlight turns on before the LCD operation of the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.
- (3) In case of Vcc = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) t4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.
- (6) CMO won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "t7 spec".



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7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

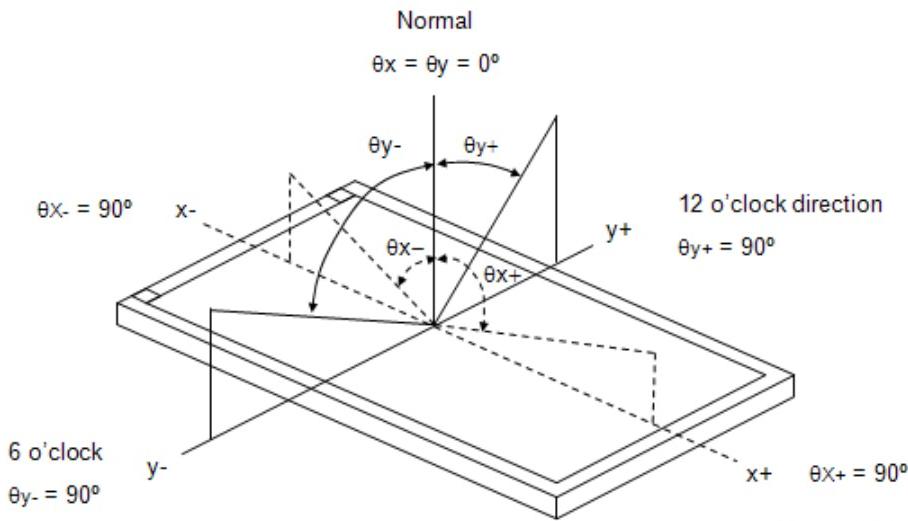
Item	Symbol	Value	Unit
Ambient Temperature	T _a	25±2	°C
Ambient Humidity	H _a	50±10	%RH
Supply Voltage	V _{CC}	5.0	V
Input Signal		According to typical value in "3. ELECTRICAL CHARACTERISTICS"	
Light Bar Input Voltage	V _{LED}	29.7	V _{DC}
Light Bar Input Current	I _{LED}	20	mA _{DC} (per string)
Duty	D	100	%

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note		
Color Chromaticity (CIE 1931)	Red	$\theta_x=0^\circ, \theta_Y=0^\circ$ CS-1000T R=G=B=255 Grayscale	Typ - 0.03	(0.636)	Typ + 0.03	---	(1), (5)		
				(0.348)					
	Green			(0.327)					
				(0.611)					
	Blue			(0.154)					
				(0.059)					
	White			0.313					
				0.329					
Center Luminance of White (Center of Screen)	L _c		200	250	---	cd/m ²	(4), (5)		
Contrast Ratio	CR		700	1000	---	---	(2), (6)		
Response Time	T _R	$\theta_x=0^\circ, \theta_Y=0^\circ$	---	1.3	2.2	ms	(3)		
	T _F		---	3.7	5.8	ms			
White Variation	δW	$\theta_x=0^\circ, \theta_Y=0^\circ$ USB2000	---	----	1.33	-	(5), (6)		
Viewing Angle	Horizontal		75	85	---	Deg.	(1), (5)		
			75	85	---				
	Vertical	$CR \geq 10$ USB2000	70	80	---				
			70	80	---				
Viewing Angle	Horizontal	$CR \geq 5$ USB2000	80	89	---	Deg.	(1), (5)		
			80	89	---				
	Vertical		75	85	---				
			75	85	---				

Note (1) Definition of Viewing Angle (θ_x , θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

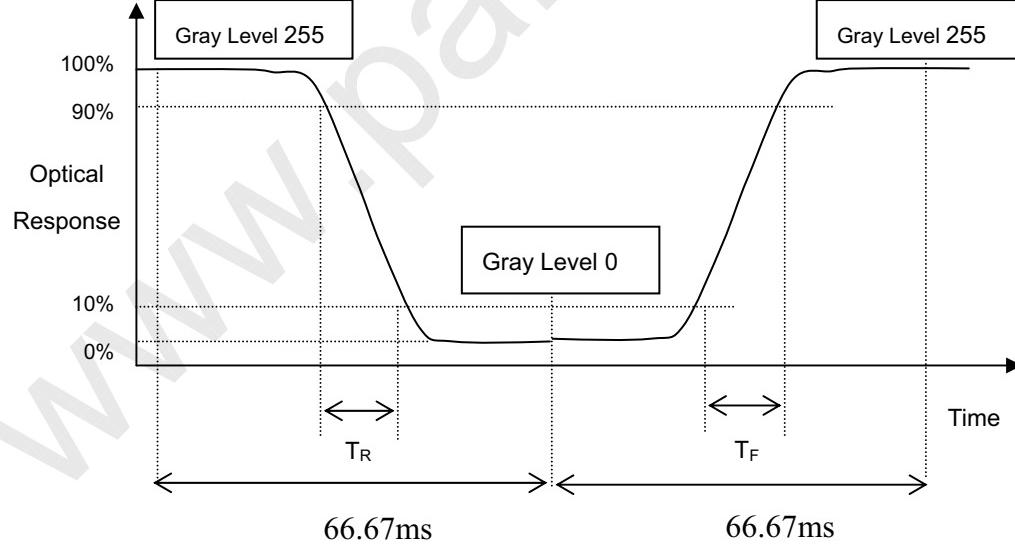
L_{255} : Luminance of gray level 255

L_0 : Luminance of gray level 0

$$CR = CR(5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R , T_F):





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Note (4) Definition of Luminance of White (L_c):

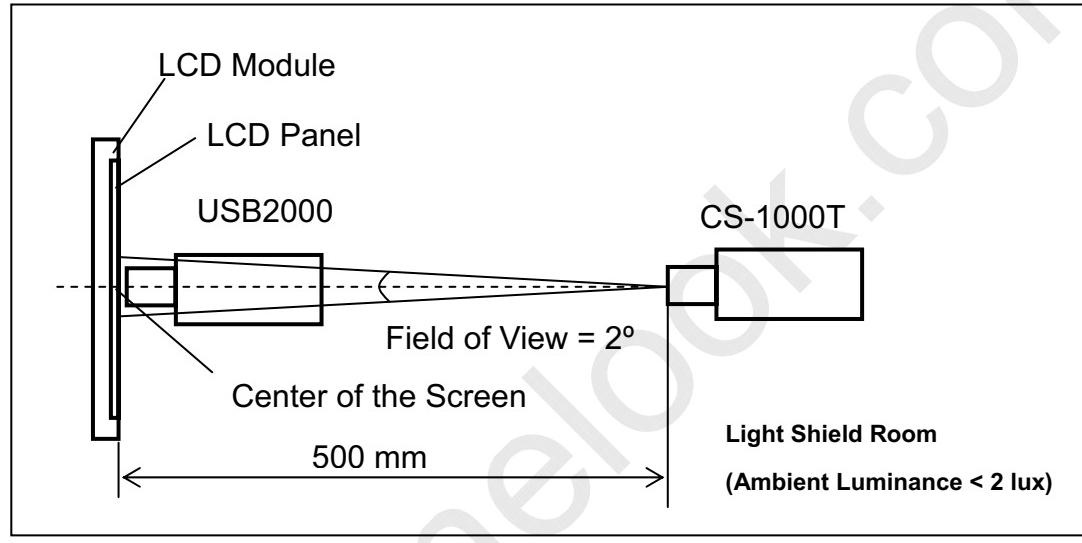
Measure the luminance of gray level 255 at center point

$$L_c = L(5)$$

$L(x)$ is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

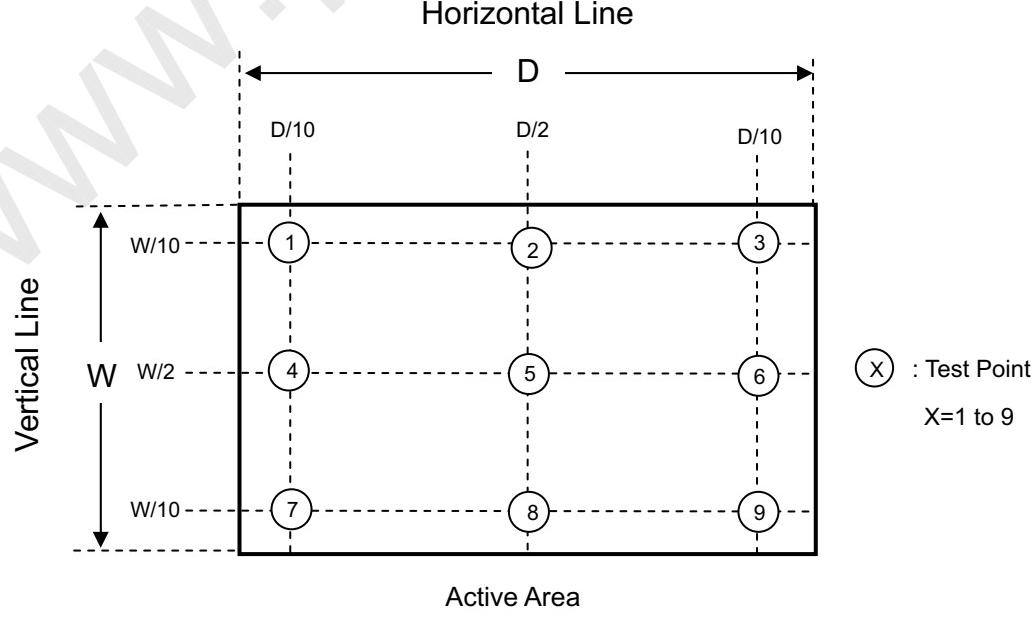
The LCD module should be stabilized at given temperature for (30 minutes) to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for (30 minutes) in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 9 points

$$\delta W = \text{Maximum } [L(1) \sim L(9)] / \text{Minimum } [L(1) \sim L(9)]$$



8. PACKAGING

8.1 PACKING SPECIFICATIONS

- (1) 12 LCD modules / 1 Box
- (2) Box dimensions: 563(L) X 390 (W) X 375 (H) mm
- (3) Weight: 24.96 Kg (12 modules per box)

8.2 PACKING METHOD

- (1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
Vibration	ISTA STANDARD Random, Frequency Range: 1 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation
Dropping Test	1 Conner, 3 Edge, 6 Face, 45.7cm (ISTA Standard)	Non Operation

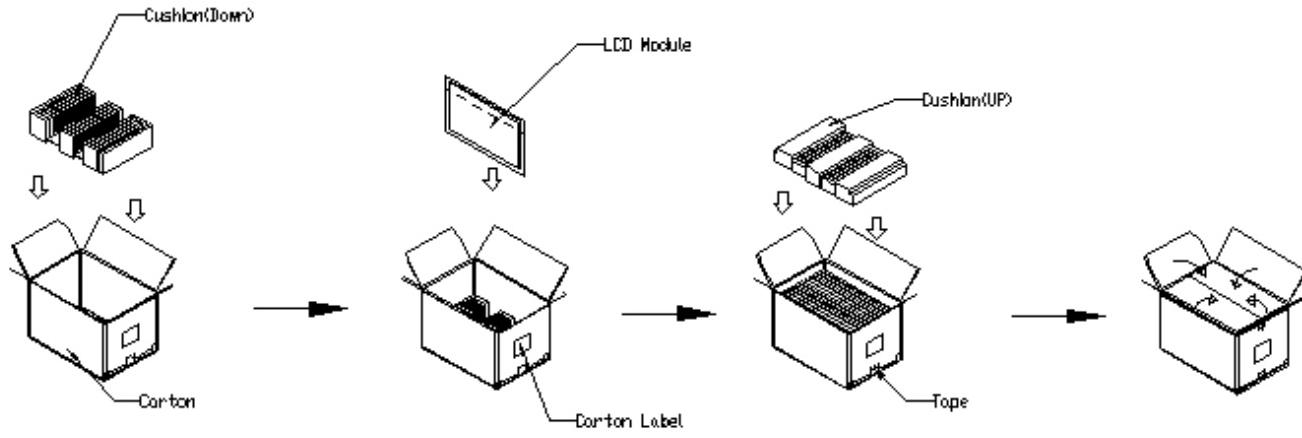
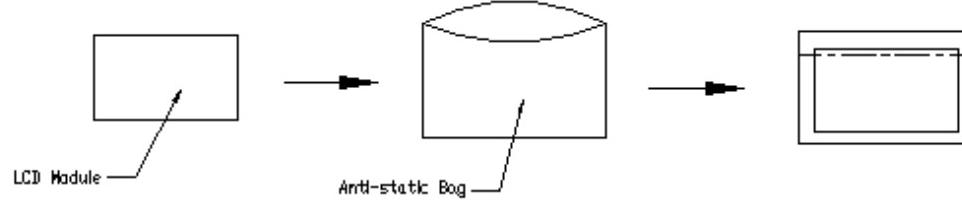
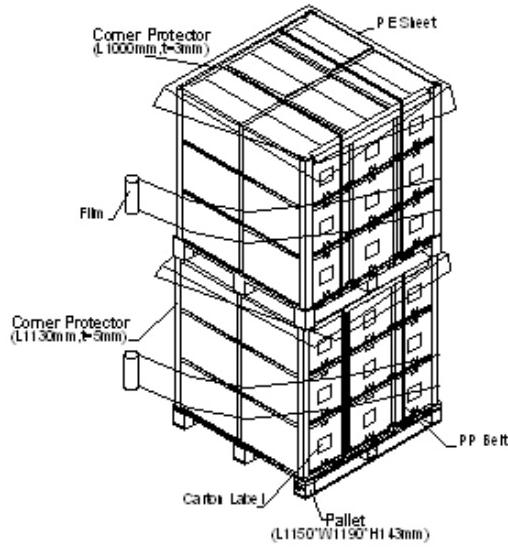


Figure. 8-1 Packing method

For ocean shipping

Sea / Land Transportation
(40ft HQ Container)



Sea / Land Transportation
(40ft Container)

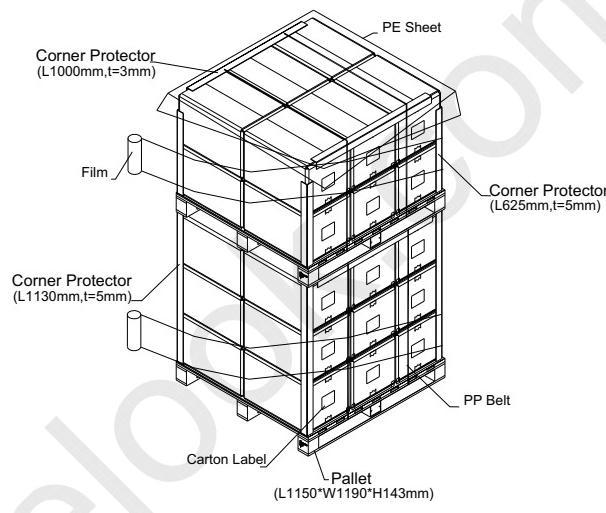


Figure. 8-2 Packing method

For air transport

Air Transportation

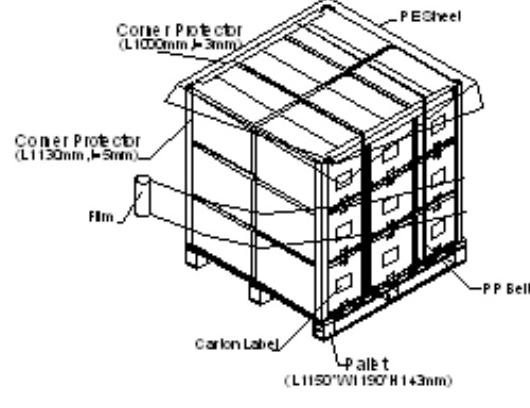


Figure. 8-3 Packing method



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9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: M215H3-L01
- (b) Revision: Rev. XX, for example: A0, A1... B1, B2... or C1, C2...etc.
- (c) CMO barcode definition:

Serial ID: XX-XX-X-XX-YMD-L-NNNN

Code	Meaning	Description
XX	CMO internal use	-
XX	Revision	Cover all the change
X	CMO internal use	-
XX	CMO internal use	-
YMD	Year, month, day	Year: 2001=1, 2002=2, 2003=3, 2004=4... Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31=1, 2, 3, ~, 9, A, B, C, ~, W, X, Y, exclude I, O, and U.
L	Product line #	Line 1=1, Line 2=2, Line 3=3, ...
NNNN	Serial number	Manufacturing sequence of product

- (d) Customer's barcode definition:

Serial ID: CM-L5H31-X-X-X-XX-L-XX-L-YMD-NNNN

Code	Meaning	Description
CM	Supplier code	CMO=CM
L5H31	Model number	M215H3-L01=L5H31
X	Revision code	ZBD, C1=A, C2=B, Non ZBD, C1=1, C2=2,
X	Source driver IC code	Century=1, CLL=2, Demos=3, Epson=4, Fujitsu=5, Himax=6, Hitachi=7, Hynix=8, LDI=9, Matsushita=A, NEC=B, Novatec=C, OKI=D, Philips=E, Renasas=F, Samsung=G, Sanyo=H, Sharp=I, TI=J, Topro=K, Toshiba=L, Windbond=M
X	Gate driver IC code	
XX	Cell location	Tainan, Taiwan=TN
L	Cell line #	1~Z
XX	Module location	Tainan, Taiwan=TN ; NB, China=NP ; NH, China=NH
L	Module line #	1~Z
YMD	Year, month, day	Year: 2001=1, 2002=2, 2003=3, 2004=4... Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31=1, 2, 3, ~, 9, A, B, C, ~, T, U, V
NNNN	Serial number	Manufacturing sequence of product

- (e) UL Factory ID:

Region	Factory ID
TWCMO	GEMN
NBCMO	LEOO
NBCME	CANO
NHCMO	CAPG


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10.0 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50°C , 80%RH, 240hours	
High Temperature Operation (HTO)	Ta= 50°C , 50%RH , 240hours	
Low Temperature Operation (LTO)	Ta= 0°C , 240hours	
High Temperature Storage (HTS)	Ta= 60°C , 240hours	
Low Temperature Storage (LTS)	Ta= -20°C , 240hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Half-sine Frequency: 10 - 300 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 11 ms Direction : ± X, ± Y, ± Z.(one time for each Axis)	
Thermal Shock Test (TST)	-20°C/30min , 60°C / 30min , 100 cycles	
On/Off Test	25°C ,On/10sec , Off /10sec , 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) Air Discharge: ± 15KV, 150pF(330Ω)	
Altitude Test	Operation:10,000 ft / 24hours Non-Operation:30,000 ft / 24hours	



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11. PRECAUTIONS

11.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

11.2 SAFETY PRECAUTIONS

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the module's end of life, it is not harmful in case of normal operation and storage.

11.3 SAFETY STANDARDS

The LCD module should be certified with safety regulations as follows:

- (1) UL60950-1 or updated standard.
- (2) IEC60950-1 or updated standard.

11.4. Storage

- (1) Do not leave the module in high temperature, and high humidity for a long time.
It is highly recommended to store the module with temperature from 0°C to 35°C
And relative humidity of less than 70%
- (2) Do not store the TFT – LCD module in direct sunlight
- (3) The module should be stored in dark place. It is prohibited to apply sunlight or fluorescent light in storing

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11.5. Operation condition guide

- (1) The LCD product should be operated under normal condition.

Normal condition is defined as below :

Temperature : $20\pm15^{\circ}\text{C}$

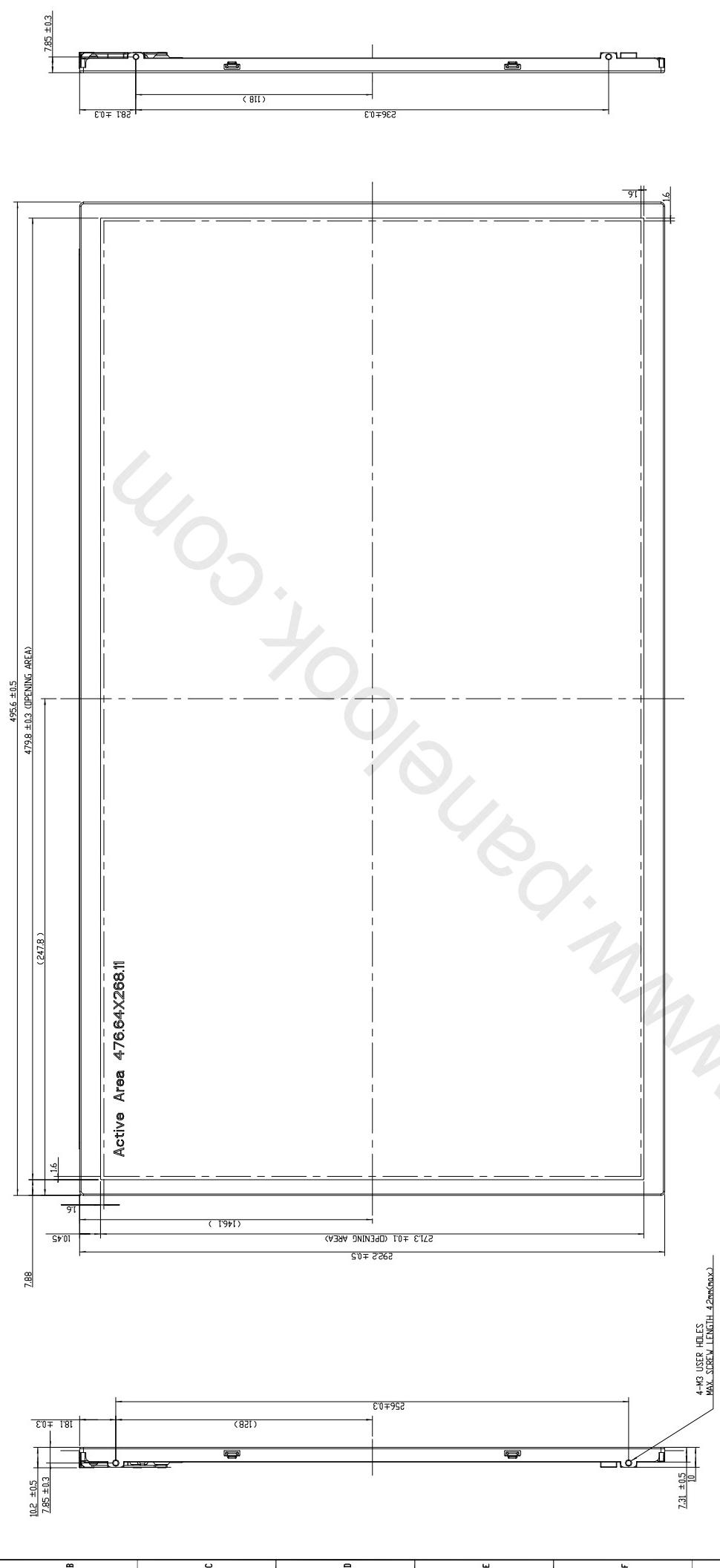
Humidity: $65\pm20\%$

Display pattern : continually changing pattern(Not stationary)

- (2) If the product will be used in extreme conditions such as high temperature , high humidity , high altitude , display pattern or operation time etc...It is strongly recommended to contact CMO for application engineering advice . Otherwise , Its reliability and function may not be guaranteed.

11.6 OTHER

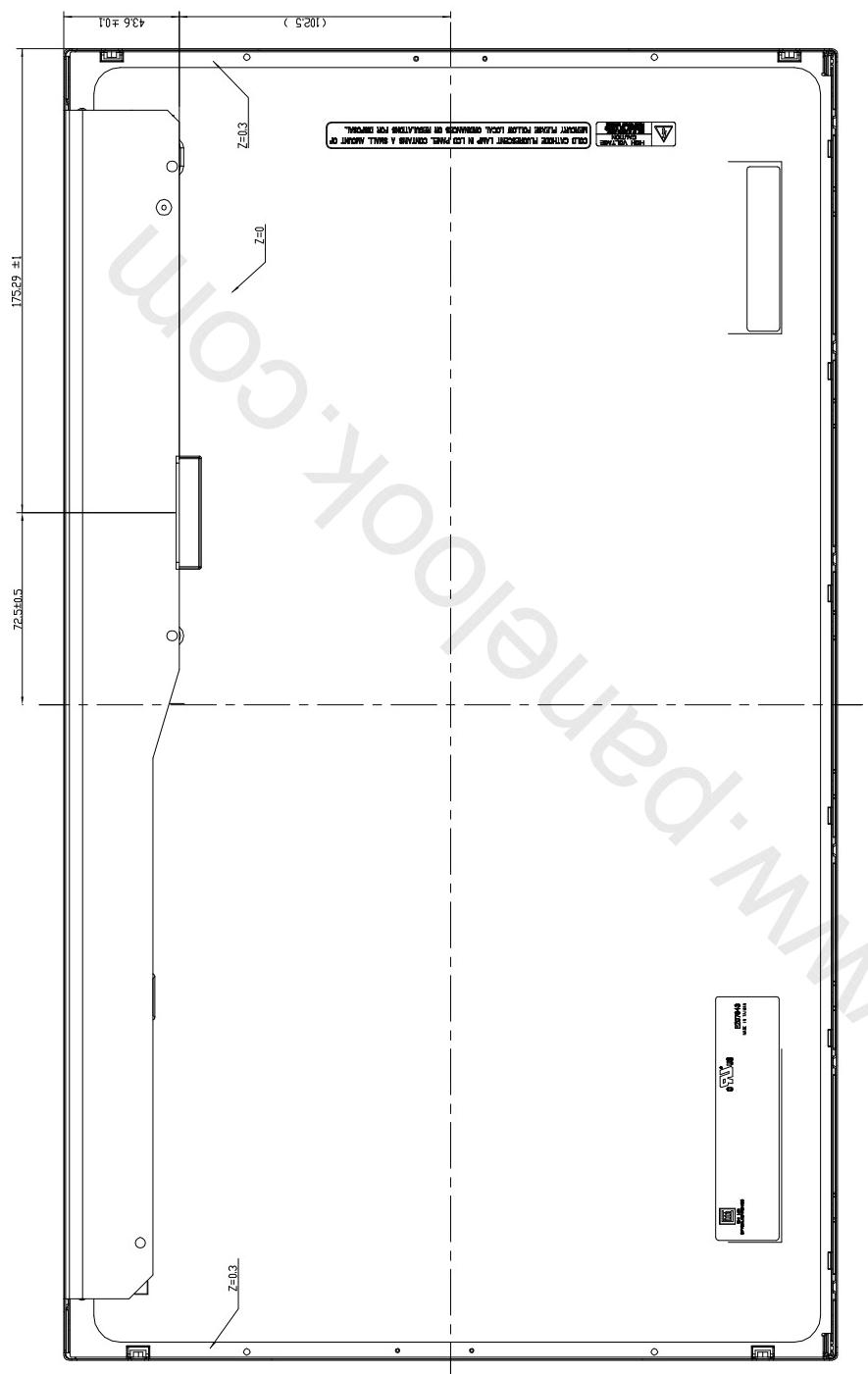
When fixed patterns are displayed for a long time, remnant image is likely to occur.



TITLE ASSY, MODULE, L2/PIN-2-01		REV. B	
Approved: YUL-LIN		Drawing No.: 1B01	
Checklist:		Print. No.: 1B01	
Driver	MAN-LIN	Part. No.	1B01
Driver	YUKE	Ver.	1B01
Driver	YUKE	Date	2009/7/26
Driver	YUKE	Scale	1:1
Driver	YUKE	Sheet	1 / 1
Driver	YUKE	Alt.	A1
Driver	YUKE	Utm	(@)
General Tolerance Unless Specified		CH1 [ME]	ALL RIGHTS RESERVED, CPTING PROHIBITED
O-6	±0.1	300-600	±0.3
6-30	±0.15	ANGLE	±1.0°
30-300	±0.2	ANGLE	±1.0°
12			1A
			15
			16

NOTES:
 1.DRILLING TOLERANCE: ±0.5mm.
 2.CUTTING TOLERANCE: ±0.5mm.
 3.LIGHTBAR CONNECTOR SPEC. STARCENN 09363-02001A OR EQUIVALENT.
 4.ACES 91500-012 Pitch 0.5mm
 5.4.5IDE MOUNT HOLE TORQUE : 5kgf·cm (MAX.).

4-M3 USER HOLES
MAX. SCREW LENGTH 4.2mm(max.)



NOTES:
 1.ULINE TOLERANCE: ±0.5mm
 2.UDLINE TOLERANCE: ±0.5mm
 3.FD: FLATNESS: SPAN CUPIDGES: 0.5mm
 4.SIDE MOUNT HOLE TORQUE : 50gf-cm MAX.

Mark		Description	Date	Chanded By	Approved By	ECN No.	Remark	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
H																								